Shri Sant Gajanan Maharaj College of Engineering Shegaon

Department of Electronics and Telecommunication Engineering

Link of PBL: https://drive.google.com/drive/folders/1xvjCE4n5EL-MuLBGcxCM0zxfCrczbWo0?usp=drive_link

Course Title & Course Code: EMBEDDED SYSTEMS (Code: 8ETC01)

Class: Final year (4U1)

Semester: VIII th

Name of the Course Teacher: Dr.K.B.Khanchandani

Title of the innovative practice: Project Based Learning

Objectives/Goals of the practice:

The primary goal of this innovative teaching practice is to develop following skills:

- Students learn how to work better in groups—providing their own input, listening to others, and resolving conflicts when they arise—they build positive relationships with teachers, which reinforces how great learning is.

- Problem Solving: Students learn how to solve problems that are important to them, including real community issues, more effectively—even learning from failure and possibly starting over.
- Creativity: Students apply creative thinking skills to innovate new product designs and possibilities for projects.
- In-Depth Understanding: Students build on their research skills and deepen their learning of applied content beyond facts or memorization.
- Self-Confidence: Students find their voice and learn to take pride in their work, boosting their agency and purpose.
- Critical Thinking: Students learn to look at problems with a critical thinking lens, asking questions and coming up with possible solutions for their project.

Use of Appropriate Methods:

To achieve the stated goals, the following steps were implemented:

- Student Project Groups were formed

- Each group consists of three to four students
- Project task is assigned to every group
- Monitoring of the task is done on regular intervals
- Final date of submission is given to them for assessment
- Based on Assesment ,marking was done.

Effective Presentation:

1. Link was shared with all students
2. Short quiz was conducted to assess student understanding

Photo of the activity

Implementation of SPI Protocol Between PC and Memory IC.
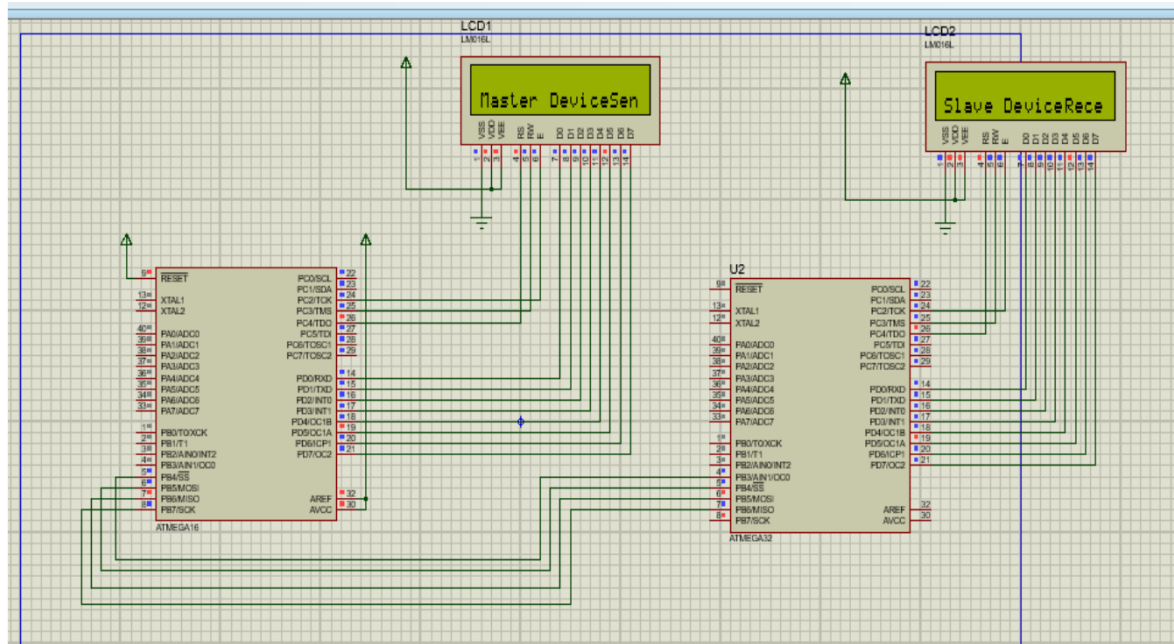
03 CIRCUIT DIAGRAM:

PO's & PSO's Mapped:

PO1, PO2, PO3,PO4, PO5, PO12, PSO1, PSO2

Reflective Critique:

The link of *PBL* was shared with other faculty members.

Mr. V. K. Bhangdiya suggested to add Real time designs of few Embedded System Applications

Evidences of success:

Increased Student Engagement :100% of students have gone through the PPTS and actively participated in simulation and hardware based real time designs**.**

Challenges faced during implementation:

--None

Link for peer review : https://forms.cloud.microsoft/r/9rUDtEbRQd